APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 08-04-2003

Application Type:: Regular

Subject Matter:: Utility

Title:: Series Capacitor Coupling

Multiplexer for Programmable Logic

Devices

Attorney Docket Number:: X-1415 US

Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 05
Small Entity?:: No

Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Robert

Middle Name:: 0.
Family Name:: Conn

Street:: 15210 Old Ranch Road

City:: Los Gatos
State or Province:: California

Postal or Zip Code:: 95033

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Kameswara

Middle Name:: K. Family Name:: Rao

Street:: 1402 Graywood Drive

City::

San Jose

State or Province::

California

Postal or Zip Code::

95129

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer

Number::

24309

ASSIGNEE INFORMATION

Assignee Name::

Xilinx, Inc.

Street::

2100 Logic Drive

City::

San Jose

State or Province::

California

Postal or Zip Code:: 95124